



# UNITED STATES PATENT AND TRADEMARK OFFICE

59  
UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/932,136	08/17/2001	Gerard Chauvel	TI-31361	5166
23494	7590	06/07/2005	EXAMINER	
TEXAS INSTRUMENTS INCORPORATED P O BOX 655474, M/S 3999 DALLAS, TX 75265			HASHEM, LISA	
			ART UNIT	PAPER NUMBER
			2645	

DATE MAILED: 06/07/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

09/932,136

Applicant(s)

CHAUVEL ET AL.

Examiner

Lisa Hashem

Art Unit

2645

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 17 August 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-13 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-13 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 17 August 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

## **DETAILED ACTION**

### ***Drawings***

1. The drawings are objected to because reference character '12' in Figure 1 is labeled MP instead of MPU. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

### ***Specification***

2. The disclosure is objected to because of the following informalities: On page 13, line 25, the line should read: 'The power compute block 66 is shown in Figure 7'. Appropriate correction is required.

***Claim Objections***

3. Claim 1 is objected to because of the following informalities: There is insufficient antecedent basis for the limitation 'the steps' in the claim. Appropriate correction is required.

***Claim Rejections - 35 USC § 102***

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claims 1-12 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by European Patent Application No. 0 683 451 A2 by Sunakawa et al, hereinafter Sunakawa.

Regarding claim 1, Sunakawa discloses a method of inherently generating energy profiles (page 9, lines 26-28) for a specific task in a processing device executing multiple tasks (see Abstract), comprising steps of: receiving a first task identifier indicative of an active task in a processing component (see Abstract; page 3, lines 27-30; page 9, lines 10-11; Figure 7, 70); storing a second task identifier indicating a task to be monitored (page 9, lines 20-25; Figure 8A, 83); comparing the first and second task identifiers and generating a predetermined signal if the first and second task identifiers match (page 9, lines 44-46); measuring activity of certain devices responsive to said predetermined signal (page 9, lines 49-57).

Regarding claim 2, the method of claim 1, wherein Sunakawa further discloses said measuring step comprises the step of enabling one or more counters responsive to said predetermined signal (page 8, lines 40-47).

Regarding claim 3, the method of claim 1, wherein Sunakawa further discloses the step of inherently updating energy profile associated with the task to be monitored (page 9, lines 26-39; page 10, lines 34-36).

Regarding claim 4, the method of claim 3, wherein Sunakawa further discloses said updating step comprises the step of inherently updating an energy profile responsive to said measuring step during operation of said processing device (page 9, line 41 – page 10, line 30).

Regarding claim 5, the method of claim 4, wherein Sunakawa further discloses the step of executing a plurality of tasks in accordance with a scenario defining scheduling of said plurality of tasks and modifying said scenario responsive to said step of inherently updating an energy profile (page 8, lines 19-25; page 8, line 56 – page 9, line 6; page 9, lines 29-39; page 10, lines 21-30).

Regarding claim 6, the method of claim 1, wherein Sunakawa further discloses and further comprising the step of performing a debugging operation responsive to said measuring step (page 8, lines 14-48).

Regarding claim 7, Sunakawa discloses a processing device for multitasking multiple tasks comprising (see Abstract): circuitry for receiving a first task identifier indicative of an active task in a processing component (see Abstract; page 3, lines 27-30; page 9, lines 10-11; Figure 7, 70); a memory for inherently storing a second task identifier indicating a task to be monitored (page 9, lines 20-25; Figure 8A, 83); a comparator for comparing the first and second task identifiers and generating a predetermined signal if the first and second task identifiers match (page 9, lines 44-46); circuitry for measuring activity of certain devices responsive to said predetermined signal (page 9, lines 49-57).

Regarding 8, the processing device of claim 7, wherein Sunakawa further discloses said measuring circuitry comprises the one or more counters that are enabled or disabled by said predetermined signal (page 8, lines 40-47).

Regarding claim 9, the processing device of claim 7, wherein Sunakawa further discloses data from said circuitry for measuring activity inherently updates an energy profile associated with the task to be monitored (page 9, lines 26-39; page 10, lines 34-36).

Regarding claim 10, the processing device of claim 9, wherein Sunakawa further discloses said energy profile is inherently updated during operation of said processing device (page 9, line 41 – page 10, line 30).

Regarding claim 11, the processing device of claim 10, wherein Sunakawa further discloses said plurality of tasks are executed in accordance with a scenario defining scheduling of said plurality of tasks and said scenario is updated responsive to said step of inherently updating an energy profile (page 8, lines 19-25; page 8, line 56 – page 9, line 6; page 9, lines 29-39; page 10, lines 21-30).

Regarding claim 12, the processing device of claim 7 and further comprising circuitry for implementing a debugging operation responsive to a value in said measuring circuitry (page 8, lines 14-48).

6. Claim 13 is rejected under 35 U.S.C. 102(b) as being clearly anticipated by European Patent Application No. 0 794 481 A2 by Gouzu et al, hereinafter Gouzu.

Regarding claim 13, a mobile communications device or portable telephone (page 2, lines 7-11) inherently comprising: an antenna for receiving and transmitting signals; and receiver/transmitter circuitry coupled to said antenna for sending and receiving audio and data

Art Unit: 2645

signals (these features are inherently found in a portable telephone in order for said telephone to function as a mobile communication device), said receiver/transmitter circuitry including a processing circuit comprising (see Figures 4-5; page 7, line 8 – page 8, line 46): circuitry for receiving a first task identifier indicative of an active task in a processing component (Figure 4, 3: Task Request Means; page 4, lines 7-8; page 8, line 19); a memory in the comparator for storing a second task identifier indicating a task to be monitored (Figure 4, 6: Task Managing Means; page 8, lines 20-22; Figure 5, S213); a comparator for comparing the first and second task identifiers and generating a predetermined signal if the first and second task identifiers match (Figure 4, 6; page 8, line 23); circuitry for measuring activity of certain devices responsive to said predetermined signal (Figure 4, 202: Power Consumption Calculating Means; page 8, lines 24-46).

### ***Conclusion***

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure:

- U.S. Patent No. 5,339,445 by Gasztonyi discloses a method of reducing power consumption in a battery powered computer system that operates a plurality of application programs

8. Any response to this action should be mailed to:

Commissioner of Patents and Trademarks  
Washington, D.C. 20231

**Or faxed to:**

(703) 872-9314 (for formal communications intended for entry)

**Or call:**

Art Unit: 2645

(703) 306-0377 (for customer service assistance)

Hand-delivered responses should be brought to: Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).


9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Lisa Hashem whose telephone number is (703) 305-4302. The examiner can normally be reached on M-F 8:30-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Fan Tsang can be reached on (703) 305-4895. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-3900.

LH

lh

November 29, 2004

  
FAN TSANG  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2600